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PATENT
4100-0122P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: WOLF, Peter et al. Conf.: 2923
Int'l Appl.No. PCT/EP99/07101 Filed: Sept. 23, 1999
US Appl. No.: 09/762,210 Group: 2661
Filed US: March 2, 2001 Examiner: WILSON, Robert W.
For: PROCESS FOR DISPLAYING THE MODULATION
ERROR RATIO OF A MULTICARRIER SIGNAL

REPLY UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 2, 2004

Sir:

In reply to the Office Action dated September 8, 2004, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes:

AMENDMENTS TO SPECIFICATION

AMENDMENTS TO THE CLAIMS

AMENDMENTS TO THE DRAWINGS

REMARKS